

**INFORMATION DISCLOSURE CITATION**  
(Use several sheets if necessary)

Docket Number (Optional)  
END920010051US1

Application Number

Applicant(s)  
Murdock et al.

Filing Date

Herewith

Group Art Unit

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
GL	AA	5,576,765	11/19/96	Cheney et al.	348	407	
GL	AB	5,742,343	04/21/98	Haskell et al.	348	415	
GL	AC	5,929,911	07/27/99	Cheney et al.	348	390	
GL	AD	5,963,222	10/05/99	Cheney et al.	345	516	
GL	AE	5,973,740	10/26/99	Hrusecky	348	401	
GL	AF	6,055,012	04/25/00	Haskell et al.	348	48	
GL	AG	6,057,884	05/02/00	Chen et al.	348	416	
GL	AH	6,128,649	10/03/00	Smith et al.	709	217	
GL	AI	6,157,740	12/02/00	Buerkle et al.	382	233	

**FOREIGN PATENT DOCUMENTS**

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

GL	AJ	"Implementation Of Digital HDTV Video Decoder By Multiple Multimedia Video Processors", C. Lee, et al., IEEE Transactions on Consumer Electronics, Vol. 42, No. 3, August 1996, pp. 395-401.
GL	AK	D. Hrusecky, pending U.S. patent application Serial No. 09/237,600, filed January 25, 1999, entitled "Anti-Flicker Logic For MPEG Video Decoder With Integrated Scaling And Display Functions".

EXAMINER

*John Doe*

DATE CONSIDERED

3/8/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.